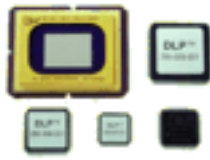


DLP® Chip Sets



	D3000 / DLi3000		CEL5500	D4100 / DLi41xx Series		
	Discovery™ 3000 Chip Set		DLP5500 Series 450	Discovery™ 4100 Chip Set		
DMD Type	0.7" XGA (1024 x 768)	0.95" SXGA+ (1400 x 1050)	0.55" XGA (1024 x 768)	0.7" XGA (1024 x 768)	0.95" 1080p (1920 x 1080)	0.96" WUXGA (1920 x 1200)
Wavelength	VIS, UV	VIS	VIS – Near IR	VIS, UV	VIS, UV	VIS, UV
Patterns/s	16,276 Hz	8,658 Hz	Up to 5,000 Hz	Up to 32,552 Hz	Up to 23,148 Hz	Up to 23,148 Hz
Data Rate	12.8 Gb/s	12.8 Gb/s	12.8 Gb/s	25.6 Gb/s	51.2 Gb/s	51.2 Gb/s
Mirror Pitch	13.68 um	13.68 um	10.8 um	13.68 um	10.8 um	10.8 um
Mirror Tilt	+/- 12 degrees	+/- 12 degrees	+/- 12 degrees	+/- 12 degrees	+/- 12 degrees	+/- 12 degrees
Mirror Blocks	16	16	16	16	16	16
DMD Package	Type A	Type A	Type S450	Type A	Type A	Type A
DMD Interface	LVDS	LVDS	LVDS	2 x LVDS	2 x LVDS	2 x LVDS
Clock Rate	200 MHz DDR	200 MHz DDR	200 MHz DDR	400 MHz	400 MHz	400 MHz
Data Lines	32 LVDS Pairs	32 LVDS Pairs	16 LVDS Pairs	32 LVDS Pairs	32 LVDS Pairs	32 LVDS Pairs
Power/Reset Driver	DAD1000	DAD1000	DLPA200	DAD2000	DAD2000	DAD2000
Digital Controller	DDC3000	DDC3000	DLPC200	DDC4100	DDC4100	DDC4100
Pixel Orientation	Orthogonal	Orthogonal	Orthogonal	Orthogonal	Orthogonal	Orthogonal
Mirror Refresh	Sequential Random Row Address	Sequential Random Row Address	Column x Column Clocking Pulse	Sequential Random Row Address	Sequential Random Row Address	Sequential Random Row Address
Reset/Settle Time	13.4 us	13.4 us	13.4 us	13.4 us	13.4 us	13.4 us

Call or Email for More Information:

(512) 615 - 4630

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